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Application Number	10/786,249-Conf. #2715	
Filing Date	February 25, 2004	
First Named Inventor	John Hayden	
Art Unit	2182	
Examiner Name	J. S. Vidwan	
Attorney Docket Number	A0312.70515US00	

ENCLOSURES (Check all that apply)						
Fee Transr	nittal Form	Drawing(s)		After Allowance Communication to TÇ		
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X Amendment/Reply		Petition		Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)		
X After	Final	Petition to Convert to a Provisional Application		Proprietary Information		
Affida	avits/declaration(s)	Power of Attorney, Revocation Change of Correspondence		Status Letter		
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Firm Name	WOLF, GREENFIELD & SACKS, P.C.					
Signature	William R. Mc Clellan					
Printed name William R. McClellar		1				
Date	October 25, 2007		Reg. No.	29,409		

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Reply under 37 CFR 1.116
— Expedited Procedure —
Technology Center 2180
cket No.: A0312 705151500

Docket No.: A0312.70515US00

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

John Hayden

Serial No.: Confirmation No.: 10/786,249

Filed:

2715 February 25, 2004

For:

DMA CONTROLLER FOR DIGITAL SIGNAL PROCESSORS

Examiner:

J. S. Vidwan

Art Unit:

2182

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AMENDMENT AFTER FINAL ACTION UNDER 37 C.F.R. 1.116

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Dear Sir:

In response to the final Office Action dated July 25, 2007, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

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